Background: The Semiconductor Industry in Texas

<table>
<thead>
<tr>
<th>236</th>
<th>+14.6%</th>
<th>$76B</th>
<th>47,000+</th>
<th>6.5%</th>
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</thead>
<tbody>
<tr>
<td>Semiconductor industry establishments across the state</td>
<td>Growth in semiconductor manufacturing establishments since passage of the CHIPS and Science Act</td>
<td>Private sector investment announced since 2021</td>
<td>Jobs in the semiconductor industry across the state</td>
<td>Increase in semiconductor jobs since passage of the CHIPS and Science Act</td>
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+ $1.4B
Texas CHIPS Act signed into legislation in 2023, established the Texas Semiconductor Innovation Consortium (TSIC) and the Texas Semiconductor Innovation Fund (TSIF), overseen by the Texas CHIPS Office

+ 3,852
Engineering degrees associated with the semiconductor industry produced by Texas universities in FY22

+ $698.3M
Texas Semiconductor Innovation Fund may be used to provide grants to state entities, such as institutions of higher education, and to business entities to encourage economic development related to semiconductor

The Investing in America agenda is promoting growth of the semiconductor industry in Texas including:

- CHIPS for America has announced proposed investments of up to $6.4 billion in proposed direct funding for Samsung Electronics to construct include two leading-edge logic foundry fabs, an R&D fab dedicated to development and, and an advanced packaging facility, as well as expand existing facilities; and
- Southern Methodist University is leading the Texoma Tech Hub, which aims to unify existing and planned semiconductor supply chain infrastructure by enhancing regional collaboration and uplifting underserved communities.

The CHIPS and Science Act (“CHIPS”) is spurring new investments in critical infrastructure, including:

- Around $260 million in infrastructure improvements near the Samsung complex in Taylor, nearly all of which will be funded by Williamson County, the city of Taylor, or the state:
  - infrastructure improvements by the City of Taylor including for water and sewers; and
  - roadway improvements and construction of the Southeast Loop connecting Taylor and Hutto to serve project sites and Samsung Highway to improve access in and out of the Taylor Fab Complex, funded in collaboration by the state and Williamson County.

CHIPS is stimulating the growth of research and development assets including:

- The Texas Semiconductor Innovation Consortium established as a forum for public and private stakeholders across the semiconductor manufacturing industry to focus on education, research and commercial production, ensuring ongoing semiconductor innovation and leadership within Texas;
- $440 million in state investment to create the Texas Institute of Electronics (TIE) at UT-Austin for supporting R&D in advanced packaging and heterogenous integration of semiconductors;
• $226.4 million in state funding for Texas A&M University includes for quantum and artificial intelligence chip fabrication and the Center for Microdevices and Systems; and
• Major university R&D centers including the Microelectronics Research Center (MRC) at The University of Texas at Austin; and the University of Texas at Dallas’s Texas Analog Center of Excellence (TxACE), North Texas Semiconductor Institute, the new Center for Harsh Environment Semiconductors and Systems (CHESS).

CHIPS is catalyzing new workforce development pipelines to connect residents with good-paying jobs in the semiconductor industry including:
• Creation of the UT Austin-Taylor Center for Semiconductor Training and Research by the University of Texas;
• A partnership between University of Texas at Austin (UT), Austin Community College District (ACC), and Texas Institute for Electronics to build joint new workforce education training programs for the semiconductor industry;
• Development of a new Career and Technical Education (CTE) Center at Taylor High School which will support training the semiconductor workforce near the new Samsung complex in Taylor;
• Expansion of collaborations between Texas State Technical College and local school districts to provide dual credit precision manufacturing pathways; and
• The North Texas Semiconductor Workforce Development Consortium (UT-Dallas, Dallas College, Collin College, North Central Texas College, Texas State Technical College and UT Arlington) seeks to triple certificates and associate degrees over the next three years.

CHIPS is mobilizing new initiatives to ensure economic benefits of semiconductor industry growth reach the whole community including:

**Fostering Sustainable Development**
The City of Taylor completed a revision of zoning policies to create a Land Development Code that facilitates more flexibility in types of housing development.

**Addressing the Childcare Crisis**
In September 2023, the Austin City Council approved an agreement to work with NXP Semiconductors in providing child care services.

**Building the STEM Education Pipeline**
Virtual reality technology is being used in programs including to teach students about manufacturing and welding; and to develop a bilingual (English/Spanish) immersive learning environment for students in the field of additive manufacturing.

**Improving Mobility**
New investments in the Austin Light Rail system and the I-35 project in Austin that are improving multimodal transportation and access to job sites for workers.
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Note: This fact sheet provides examples of entities, programs, and initiatives that CHIPS for America is aware of in the State of Texas to support its growing semiconductor industry. The information is based on CHIPS for America engagement with entities involved in Texas’s semiconductor industry and was collected in collaboration with Texas Economic Development and Tourism. Texas Economic Development and Tourism provided the list of state and local economic development organizations in geographies with leading-edge CHIPS Incentives announcements.

Inclusion in this fact sheet does not convey the CHIPS Program Office’s approval, endorsement, sponsorship, or other evaluation of any entity, program, or initiative, nor does the exclusion of any entity on the list convey any disapproval. The CHIPS Program Office makes no claims, promises, or guarantees about the completeness, accuracy, or currency of information in this fact sheet. All examples are non-exhaustive and provided for informational purposes only.

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1. QCEW 2022 Annual Average Establishments for NAICS 334413
2. QCEW 3Q 2023 less 3Q 2022 for NAICS 334413
3. QCEW 2023 NAICS 334413 preliminary data