CHIPS R&D STANDARDS SUMMIT AGENDA – DAY 1

TUESDAY, SEPTEMBER 26 (8:30AM – 5:30PM ET)

TIME	SESSION TITLE (TENTATIVE SCHEDULE)
8:30am – 9:30am	**Overview and Keynotes** This session will establish the motivation, purpose, and objectives for convening the CHIPS R&D Standards Summit. Keynote addresses will include CHIPS R&D Director Lora Weiss on the goals of the CHIPS R&D programs and the role of technical standards, NIST Senior Advisor Jayne Morrow on the United States Government National Standards Strategy for Critical and Emerging Technologies, and SEMI Americas President Joe Stockunas on the importance of standards in the semiconductor and microelectronics industries.
9:30am – 10:00am	**Break**
10:00am – 12:00pm	**Strategic Standards Priorities – Topic Identification and Scoping** **Part 1 – Topic Identification:** This panel will focus on surfacing topic areas that are high strategic priorities for the community in focusing on standards that drive innovation and speed commercialization. Examples of topic areas include complex integrated systems, materials, design, fabrication, assembly packaging, equipment, modeling, data (including parametric data), and performance or conformance testing. **Part 2 – Topic Scoping:** Panelists will explore the appropriate scope for the strategic priority areas that emerged in the preceding session. Examples might include whether a packaging priority could focus on advanced 3D architectures or cover a broader set; or whether a data priority could focus on real-time process data, machine learning for automation, or other areas. The outcomes of this session will inform the focus of subsequent workshops.
12:00pm – 1:30pm	**Lunch Break**

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TIME	SESSION TITLE (TENTATIVE SCHEDULE)
1:30pm – 2:30pm	**Innovation in Standards Development, to include Incubators and Accelerators** This panel will consider whether concepts from incubators and accelerators, as practiced in the technology sector, might be adapted for use in standards development to allow standards processes to keep pace with innovation, broaden opportunities for participation in standards activities, and provide educational and training opportunities. Existing efforts – such as iNEMI (TIG teams, TCs, Research Committee); IEEE-ISTO Alliances and Consortia; Joint Development Foundation Projects; and SEMI Technical Committees – will be considered as examples to inform the discussion.
2:30pm – 3:00pm	**Break**
3:00pm – 4:00pm	**Diverse, Standards-Capable Workforce** This panel will examine approaches for enabling a diverse, standards-capable workforce that can bring the range of skills and perspectives needed to meet the fast-paced and complex standards needs of the semiconductor and microelectronics sector. Speakers from educational institutions, industry, and standards setting organizations will discuss standards priorities in developing content for the microelectronics industry workforce.
4:00pm – 5:00pm	**Pre-Standards Research** This panel will focus on pursuing the research needed to provide the science and technology foundations for sound, effective, and relevant standards. Examples from the CHIPS R&D Metrology Program, the National Nanotechnology Coordination Office (NNCO), and the Networking and Information Technology Research and Development (NITRD) Program will be considered to inform discussion of this topic.
5:00pm – 5:30pm	**Day 1 Closing**

CHIPS R&D STANDARDS SUMMIT AGENDA – DAY 2

WEDNESDAY, SEPTEMBER 27 (8:30AM – 5:30PM ET)

TIME	SESSION TITLE (TENTATIVE SCHEDULE)
8:30am – 9:00am	**Overview and Keynotes** NIST and CA moderators summarize Day 1 highlights and review the Day 2 agenda before resolving attendee questions.
9:00am – 9:15am	**Break**
9:15am – 10:15am	**Strategic Standards Priorities – Topics (Breakout)** This breakout session will focus on surfacing topic areas that are high strategic priorities for the community in focusing on standards that drive innovation and speed commercialization. Examples of topic areas include complex integrated systems, materials, design, fabrication, assembly packaging, equipment, modeling, data (including parametric data), and performance or conformance testing. While detailed, specific standards needs will be relevant as examples in this discussion, the focus is on identifying higher-level categories for a deeper dive in subsequent sessions and follow-on workshops.
10:15am – 11:15am	**Standards Priority Areas – Scoping (Breakout)** This breakout session will explore the appropriate scope for the strategic priority areas that emerged in the preceding session. Examples might include whether a packaging priority could focus on advanced 3D architectures or cover a broader set; or whether a data priority could focus on real-time process data, machine learning for automation, or other areas. The outcomes of this session will inform the focus of subsequent workshops.
11:15am – 12:45pm	**Lunch Break**

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WEDNESDAY, SEPTEMBER 27 (8:30AM – 5:30PM ET)

TIME	SESSION TITLE (TENTATIVE SCHEDULE)
12:45pm – 1:45pm	**Innovation in Standards Development, to include Incubators and Accelerators (Breakout)** This breakout session will consider whether concepts from incubators and accelerators, as practiced in the technology sector, might be adapted for use in standards development to allow standards processes to keep pace with innovation, broaden opportunities for participation in standards activities, and provide educational and training opportunities. Existing efforts – such as iNEMI (TIG teams, TCs, Research Committee); IEEE-ISTO Alliances and Consortia; Joint Development Foundation Projects; and SEMI Technical Committees – will be considered as examples to inform the discussion.
1:45pm – 2:15pm	**Break**
2:15pm – 2:30pm	**Recap of Strategic Standards Priorities** This session will review the inputs from the strategic standards priorities breakout session discussions and prepare attendees for the following breakout session discussions.
2:30pm – 2:40pm	**Remarks from U.S. Department of Commerce Secretary Gina Raimondo** The session will include comments from U.S. Secretary of Commerce, Gina Raimondo, on the CHIPS for America program, including the role of standards in promoting innovation and advancement.
2:45pm – 3:45pm	**Diverse, Standards-Capable Workforce (Breakout)** This breakout session will examine approaches for enabling a diverse, standards-capable workforce that can bring the range of skills and perspectives needed to meet the fast-paced and complex standards needs of the semiconductor and microelectronics sector. Speakers from educational institutions, industry, and standards setting organizations will discuss standards priorities in developing content for the microelectronics industry workforce.

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TIME	SESSION TITLE (TENTATIVE SCHEDULE)
3:45pm – 4:45pm	**Pre-Standards Research (Breakout)** This panel will focus on pursuing the research needed to provide the science and technology foundations for sound, effective, and relevant standards. Examples from the CHIPS R&D Metrology Program, the National Nanotechnology Coordination Office (NNCO), and the Networking and Information Technology Research and Development (NITRD) Program will be considered to inform discussion of this topic.
4:45pm – 5:00pm	**Break**
5:00pm – 5:30pm	**Wrap-up and Next Steps (Day 2 Closing)**