NIST on a Chip Workshop  
February 28 & March 1, 2018  
Agenda Outline  
More details to come

**Day 1:**

8:00 am to 8:30 am  Gathering, light refreshments

8:30 am to 8:40 am  Welcome

8:40 am to 9:15 am  Overview of NIST on a Chip program and goals for Workshop

9:15 am to 10:15 am  NIST on a Chip project talks (three)

10:15 am to 10:35 am  Break with refreshments

10:35 am to 10:55 am  NIST on a Chip project talks (three)

11:35 am to 12:30 pm  Facilitated Panel Discussion #1: Scientific panel from NIST, possibly scientists from industry, universities, etc. What do you see as the scientific and technical opportunities for NIST on a Chip and similar quantum technologies? What do you imagine the future of NIST on a Chip metrology and sensing to be? What are some of the key challenges and opportunities?

12:30 pm to 1:30 pm  Lunch. Opportunities for informal networking and discussion.

1:30 pm to 3:30 pm  Poster session of NIST on a Chip and related projects. Opportunities for more in-depth discussions. Light refreshments available.

3:30 pm to 5:00 pm  Facilitated Panel Discussion #2: Industry and Federal agency representatives. Moderated by NIST facilitator. What do you see as the general opportunities, challenges, potential impacts of NIST on a Chip for your company/program, for US technologies, etc.

5:00 pm  Reception to continue informal discussions.
Day 2:

8:00 am to 8:30 am  Gathering, light refreshments

8:30 am to 8:45 am  Welcome to second day, summary of yesterday’s events.

8:45 am to 9:15 am  Talk from industry about NIST on a Chip type technologies.

9:15 am to 9:45 am  Talk from Federal agency about NIST on a Chip type technologies.

9:45 am to 10:00 am  Break with light refreshments.

10:00 am to 12:30 pm  Individual/small group interaction opportunities.
  •  Lab tours, including demonstrations from other NIST Boulder where possible. Limited numbers of visitors, previous sign up required, perhaps after Day 1 poster session.
  •  Informal individual meetings.

12:30 pm to 1:30 pm  Lunch. Opportunities for informal networking and discussion.

1:30 pm to 3:00 pm  Facilitated Panel Discussion #3: Industry, Federal agency, and NIST representatives. Provide your advice and guidance to NIST about current status and future directions for NIST on a Chip program. Consider scientific/technical directions, interactions with external partners, planning, etc. What is NIST doing right? What should NIST do differently? What are your specific recommendations for future of NIST on a Chip program?

3:00 pm to 3:30 pm  Wrap up. Brief discussion of lessons learned, potential future directions, next steps

3:30 pm onward  “Formal” workshop ends by 3:30 pm. But participants can stay for individual discussions, individual lab visits, etc.