

**Project Title: Photonic Systems Manufacturing Consortium (PSMC)**

**Lead: International Electronics Manufacturing Initiative, Inc. (iNEMI)**

**Award Number: 70NANB14H053**

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**Website: [www.photonicsmanufacturing.org](http://www.photonicsmanufacturing.org)**

#### Scheduled Meetings of the Photonic Systems Manufacturing Consortium (PSMC)

The Photonic Systems Manufacturing Consortium will invite all previous participants in relevant iNEMI, ITRS, and MIT Microphotonics Technology Working Groups (TWGs) and additional critical stakeholders to participate in the focused “integrated photonics” effort. Key organizations including universities, research institutes, SMEs, customers, and the vertical technology supply chain will be invited to participate. The PSMC will hold three workshops to review the PSMC Roadmap status and to gain input from key stakeholders. These meetings will take place in Cambridge Massachusetts in conjunction with the spring and fall meetings of the MIT Microphotonics Center. Select meetings will be presented as WebEx events to allow for remote participation. Scheduled Workshops to support each TWG will be held by WebEx attendance to receive and evaluate expert content for PSMC Roadmap content.

The first workshop will be on November 6-7, 2014. The second workshop will be in May 2015 and the third will be in October-November 2015. Attendance at the workshops and all meetings of PSMC is by invitation. We welcome all stakeholders who wish to contribute to the goals of the consortium to participate in our activities. Requests to receive an invitation to join a workshop will be processed on the PSMC website (which will be operational shortly) at [www.photonicsmanufacturing.org](http://www.photonicsmanufacturing.org)

The Photonic Systems Manufacturing Consortium utilizes five Technology Working Groups or TWGs to develop the roadmap. To receive an invitation to join one of these groups, contact the TWG leader listed in Table 1. These meeting will primarily be webinars and a calendar of TWG meetings will be provided to TWG members in the “members” section of our web site. Meeting schedules will be determined by individual TWG Chairs.

<b>TWG</b>	<b>Chair</b>	<b>Scope</b>
Hybrid Integration	Dick Otte <a href="mailto:otte@promex-ind.com">otte@promex-ind.com</a>	Cost, integration, assembly, test, functionality, tools
Packaging	Bill Bottoms <a href="mailto:bill_bottoms@3mts.com">bill_bottoms@3mts.com</a>	Cost, materials, heat, footprint, port count, bandwidth
Connector & Substrate	John MacWilliams <a href="mailto:jmacwilliams@bishopinc.com">jmacwilliams@bishopinc.com</a>	All separable interfaces within the system scope All circuit board and backplane components

Monolithic Integration	Lionel Kimerling <a href="mailto:lckim@mit.edu">lckim@mit.edu</a>	Chips: silicon photonics, InP; Design for Manufacturing; Tradeoffs for cost, bandwidth density, power efficiency, and functional latency
Functions and Market Needs	Richard Grzybowski	Application needs; system performance targets; understanding the consequences of parallelism; virtualization; and software-defined networks

**Table 1: Five PSMC Technology Working Groups (TWGs)**