

NIST Workshop on Hardware CPE and CVSS Updates

Agenda

Monday, June 22, 2026 · 10:00 a.m. – 4:00 p.m. ET

Virtual

[Event page on nist.gov](#)

· Questions: cpe-workshop@nist.gov

Time (ET)	Session	Lead
10:00 – 10:15 a.m.	Welcome & framing	Kevin Stine (NIST)
10:15 – 10:45 a.m.	Why CPE is being revised (the issues)	Dragos Prisaca (NIST)
10:45 – 11:15 a.m.	CPE 3.x: issues & directions	Harold Booth (NIST)
11:15 – 11:30 a.m.	<i>Break</i>	
11:30 a.m. – 12:00 p.m.	Identifying & matching hardware	Lexi Selldorff (Manifest Cyber)
12:00 – 1:00 p.m.	Facilitated morning discussion – open discussion of the morning talks; share your hardware use cases and what you would want from a revised CPE. Input-gathering, not approval.	Facilitator: JP Chalpin (CyberESI)
1:00 – 2:00 p.m.	<i>Lunch</i>	
2:00 – 2:45 p.m.	Panel: Identify hardware and vulnerabilities in practice	Mod: Kostas Amberiadis (NIST) · Daniel O'Loughlin (Qualcomm), Arun Kanuparthi (Intel), Ankur Srivastava (UMD), Swarup Bhunia (UF)
2:45 – 3:00 p.m.	<i>Break</i>	
3:00 – 3:45 p.m.	Panel: Severity scoring & hardware	Mod: Kostas Amberiadis (NIST) · Jonathan Spring (SSVC), Jay Jacobs (EPSS), Michael Zuzak (RIT), JV Rajendran (TAMU)
3:45 – 4:00 p.m.	Closing & next steps	