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Hardware Write Blocker Device (HWB)

Specification

Version 1.0



National Institute of Standards and Technology
Technology Administration, U.S. Department of Commerce

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56

57 **1 Introduction**

58 There is a critical need in the law enforcement community to ensure the reliability of
59 computer forensic tools. A capability is required to ensure that forensic tools consistently
60 produce accurate and objective test results. The goal of the Computer Forensic Tool Testing
61 (CFTT) project at the National Institute of Standards and Technology (NIST) is to establish a
62 methodology for testing computer forensic tools by the development of functional
63 specifications, test procedures, test criteria, test sets, and test hardware. The results provide
64 the information necessary for toolmakers to improve tools, for users to make informed
65 choices about acquiring and using computer forensics tools, and for interested parties to
66 understand the tools' capabilities. This approach for testing computer forensic tools is based
67 on well-recognized international methodologies for conformance testing and quality testing.
68 This project is further described at <http://www.cftt.nist.gov/>.

69

70 The CFTT is a joint project of the National Institute of Justice (NIJ), the research and
71 development organization of the U.S. Department of Justice; NIST's Office of Law
72 Enforcement Standards (OLES) and Information Technology Laboratory (ITL); and is
73 supported by other organizations, including the Federal Bureau of Investigation, the
74 Department of Defense Cyber Crime Center, and the Department of Homeland Security's
75 Bureau of Immigration and Customs Enforcement and U.S. Secret Service. Since all
76 documents are posted on the web for public review, the entire computer forensics community
77 participates in the development of the specifications and test methods.

78

79 The central requirement for a sound forensic examination of digital evidence is that the
80 original evidence must not be modified, i.e., the examination or capture of digital data from
81 the hard drives or other storage media of a seized computer must be performed so that the
82 contents are not changed. The investigator follows a set of procedures designed to prevent the
83 modification of original evidence. These procedures may include various write blocking
84 techniques including using a software tool or hardware device to block modification of the
85 contents of a drive.

86

87 **2 Purpose**

88 This document defines functional requirements for hardware write blocker (HWB) devices
89 used in computer forensics investigations.

90

91 These requirements will be used to derive test assertions, test cases, and a test plan. The test
92 assertions, test cases, and test plan will be published as a separate document. The
93 requirements were developed by a focus group of individuals who are experts in the use of
94 hardware write blocking tools and have performed investigations that have depended on the
95 results of these tools. As this document evolves through comments from the focus group and
96 others, new versions will be posted to our web site at <http://www.cftt.nist.gov>.

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99

100 3 Background Information

101

102 Data is written to or read from a storage device via commands that are issued by the
103 computer and transmitted from the computer's interface connection to the storage device's
104 interface connection. A hardware write blocker (HWB) is a hardware device that attaches to
105 a computer system with the primary purpose of intercepting and preventing (or 'blocking')
106 any modifying commands from ever reaching the storage device. Physically, the device is
107 connected between the computer and a storage device. Some of its functions include
108 monitoring and filtering any activity that is transmitted or received between its interface
109 connections to the computer and the storage device.

110

111 The interface connections do not have to be the same type. The computer connection to a
112 HWB could be using a SCSI interface while the HWB connection to the hard disk could be
113 using an IDE interface. Any assumptions that are made about either the data that the HWB is
114 protecting or about the functions of the HWB, itself, are based entirely on the notion that the
115 capabilities of the HWB are limited by the capabilities of its interfaces. [See Addendum for
116 examples of interface commands]

117

118 4 Terminology

119

120 Included here are definitions that define key terms or variations of key terms used in this
121 specification. Most definitions are from the Working draft document (WD) of the Millennial
122 Edition of the American National Standard Dictionary of Information Technology
123 (ANSDIT), developed by the American National Standards Institute (ANSI), National
124 Committee for Information Technology Standards (NCITS), the Technical Committee on
125 Vocabulary, K5. The ANSDIT has been harmonized with ISO/IEC-2382, Information
126 Technology Vocabulary (ITV). [http://www.ncits.org/tc_home/k5htm/Ansdit.htm]

127

128 **command:** (1)An order for an action to take place. (2) A control signal. ... [ANSI]

129

130 **firewire:** A colloquial term referring to an [external bus](#) standard that supports [data transfer](#)
131 [rates](#) of up to 400Mbps (IEEE Standard 1394a) and 800Mbps (IEEE Standard
132 1394b). The term 'FireWire' was trademarked by Apple.

133

134 **firmware:** An ordered set of instructions and associated data stored in a manner that they are
135 considered part of the hardware configuration as distinct from the software that is
136 dynamically loaded as needed; for example, microprograms stored in read-only
137 memory. Firmware may be implemented in hardware logic or stored in read-only
138 memory; it may be addressed as part of the memory address space or be entirely
139 separate. [ANSI]

140

141 **Integrated Drive Electronics/AT Attachment (IDE/AT) Interface:** A colloquial term for
142 interface standards developed by T13. Technical Committee T13 is responsible for all
143 interface standards relating to the AT Attachment (ATA) storage interface utilized as
144 the disk drive interface on personal and mobile computers. T13 is a Technical
145 Committee for the InterNational Committee on Information Technology Standards

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146 (INCITS) [<http://www.incits.org/>]. INCITS is accredited by, and operates under rules
147 approved by, the American National Standards Institute (ANSI)
148 [<http://www.ansi.org/>]. [see Addendum for ATA interface commands]
149

150 **interface:** A shared boundary defined by the characteristics of that boundary. The interface
151 may be described at the physical level, at the software level, or as purely logic
152 operations. For example, characteristics of the boundary may include the
153 identification of any physical interconnections, description of signal exchanges across
154 the boundary, or specification of functions performed on each side of the boundary.
155 [ANSI]
156

157 **modification:** (1) An addition or change to stored data or a deletion of stored data. ...[ANSI]
158

159 **read:** To obtain data from an input device, from a storage device, or from a data medium.
160 [ANSI]
161

162 **storage device:** A functional unit into which data can be placed, in which they can be
163 retained, and from which they can be retrieved. [ANSI]
164

165 **protected storage device:** A storage device whose interface is connected to a HWB.
166

167 **Small Computer System Interface (SCSI):** A colloquial term for interface standards
168 developed by T10. Technical Committee T10 is responsible for SCSI Storage
169 Interfaces and SCSI architecture standards (SAM, SAM-2, and SAM-3), which are
170 used by SCSI, SAS, Fibre Channel, SSA, IEEE 1394, USB, and ATAPI. T10 is also
171 responsible for many SCSI command set standards (e.g., SPC, SPC-2, SPC-3, SBC,
172 SBC-2, SSC, SSC-2, SSC-3, MMC, MMC-2, MMC-3, MMC-4, RBC, etc.). T10 is a
173 Technical Committee of the [InterNational Committee on Information Technology
174 Standards \(INCITS\)](http://www.incits.org/) [<http://www.incits.org/>]. INCITS is accredited by, and operates
175 under rules that are approved by, the [American National Standards Institute \(ANSI\)](http://www.ansi.org/)
176 [<http://www.ansi.org/>]. [see Addendum for SCSI interface commands]
177

178 **transmit:** To send from one location for reception elsewhere. ... [ANSI]
179

180 **Universal Serial Bus (USB):** A colloquial term referring to [external bus](#) standards that
181 support [data transfer rates](#) of up to 480 [Mbps](#) for high-speed connection of peripheral
182 equipment to microcomputers.
183

184 **write:** To send data to an output unit, to a storage device, or to a data medium. [ANSI]
185

186 5 Scope

187 The scope of this specification is limited to hardware devices that protect the contents of a
188 computer hard drive or other storage media. The specifications are general and are based on
189 the following assumptions.
190

191 1. Operations that could modify data on the storage device are controllable at the interface
192 level (ie, outside of the storage device itself). Any possible operations that can take place

- 193 inside of the storage device that are not accessible or controllable via the interface
194 functionality are outside the scope of this specification.
195
- 196 2. Any backward compatibility of a given HWB device is based primarily on the backward
197 compatibility of its implemented interfaces. If the interface specifications mandate certain
198 backward compatibilities, the assumption is that those backward compatibilities exist.
199
- 200 3. All devices are in a working computer system configuration. At a very minimum, a device
201 is considered as being in a "working" state if it is connected to a powered-on host system
202 and can receive interface commands and issue a response for those commands.
203
- 204 4. Any changes to the computer system configuration to install the HWB must be technically
205 sound and compatible with the respective interface specifications. An example of changes
206 to the configuration would be the installation of a PCI-SCSI adapter card to support a
207 SCSI-IDE HWB device.
208
- 209 5. The HWB is being used in a non-hostile environment. The assumption is that the
210 environment in which these devices are used is controlled by individuals that are adhering
211 to the intended use of the device.
212
- 213 6. The scope of the specification will be limited to the following interfaces: ATA, SCSI,
214 USB, and Firewire.
215
216

217 6 Requirements

218
219 General hardware write blocker (HWB) functions could be described as: a HWB should not
220 allow modifying commands to be transmitted to a storage device and should allow non-
221 modifying commands to be transmitted a storage device.
222

223 Non-modifying commands are those that either read or gather information from a storage
224 device. Modifying commands are all others including those that write to a storage device.
225 Examples of ATA and SCSI interface commands are given in the addendum.
226

227 6.1 Requirements for Mandatory Features

228
229 **HWB-RM-01** A HWB shall block modifying commands to a protected storage device.
230

231 **HWB-RM-02** A HWB shall allow non-modifying commands to be transmitted to a protected
232 storage device.
233

234 **HWB-RM-03** A non-modifying command that enters the HWB shall be equivalent to the
235 command that exits the HWB.
236

237 **HWB-RM-04** The response that is transmitted from the protected storage device to the HWB
238 shall be equivalent to what is transmitted from the HWB to the computer.

239

240 **6.2 Requirements for Optional Features**

241

242 The following requirements define optional device features. If a HWB device provides the
243 capability defined, then the tool will be tested as if the requirement were mandatory. If the
244 device does not provide the capability defined, the requirement will not apply.

245

246 **HWB-RO-01** A HWB shall provide the capability to have a storage device either protected
247 or not protected.

248

249 **HWB-RO-02** A HWB shall provide the capability to protect a storage device's firmware.

250

251 **HWB-RO-03** A HWB shall provide the capability to indicate a successful response for
252 blocked commands.

253

254 **HWB-RO-04** A HWB shall provide the capability to indicate a failed response for blocked
255 commands.

256

257 **HWB-RO-05** A HWB shall provide the capability to indicate that it is operational.

258

259 Addendum - Interface Command Examples

260

261 These tables are listings of some ATA and SCSI commands. A HWB device should block
 262 modifying commands and not block non-modifying commands. Testing commands in the
 263 configuration category will be discussed in the test plan for HWB.

264

265 ATA

| Command | Hex Code | Category |
|----------------------------------|----------|---------------|
| CFA Erase Sectors | C0h | Modifying |
| CFA Request Extended Error Code | 03h | Non-modifying |
| CFA Translate Error | 87h | Non-modifying |
| CFA Write Multiple Without Erase | CDh | Modifying |
| CFA Write Sectors Without Erase | 38h | Modifying |
| Check Power Mode | E5h | Non-modifying |
| Device Reset | 08h | Configuration |
| Download Microcode | 92h | Modifying |
| Execute Device Diagnostic | 90h | Non-modifying |
| Flush Cache | E7h | Modifying |
| Get Media Status | DAh | Non-modifying |
| Identify Device | ECh | Non-modifying |
| Identify Packet Device | A1h | Non-modifying |
| Idle | E3h | Non-modifying |
| Idle Immediate | E1h | Non-modifying |
| Initialize Device Parameters | 91h | Configuration |
| Media Eject | EDh | Non-modifying |
| Media Lock | DEh | Non-modifying |
| Media Unlock | DFh | Non-modifying |
| NOP | 00h | Non-modifying |
| Packet | A0h | Non-modifying |
| Read Buffer | E4h | Non-modifying |
| Read DMA | C8h | Non-modifying |
| Read DMA Queued | C7h | Non-modifying |
| Read Multiple | C4h | Non-modifying |
| Read Native Max Address | F8h | Non-modifying |
| Read Sector(s) | 20h | Non-modifying |
| Read Verify Sector(s) | 40h | Non-modifying |
| Security Disable Password | F6h | Configuration |
| Security Erase Prepare | F3h | Configuration |
| Security Erase Unit | F4h | Modifying |
| Security Freeze Lock | F5h | Modifying |
| Security Set Password | F1h | Modifying |
| Security Unlock | F2h | Configuration |
| Seek | 70h | Non-modifying |

| | | |
|---|-----|---------------|
| Service | A2h | Configuration |
| Set Features | EFh | Configuration |
| Set Max Address | F9h | Configuration |
| Set Max Set Password | F9h | Configuration |
| Set Max Lock | F9h | Configuration |
| Set Max Unlock | F9h | Configuration |
| Set Max Freeze Lock | F9h | Configuration |
| Set Multiple Mode | C6h | Configuration |
| Sleep | E6h | Configuration |
| SMART Disable Operations | B0h | Configuration |
| SMART Enable/Disable Attribute Autosave | B0h | Configuration |
| SMART Enable Operations | B0h | Configuration |
| SMART Execute Off-line Immediate | B0h | Configuration |
| SMART Read Data | B0h | Non-modifying |
| SMART Read Log | B0h | Non-modifying |
| SMART Return Status | B0h | Non-modifying |
| SMART Save Attribute Values | B0h | Modifying |
| SMART Write Log | B0h | Modifying |
| Standby | E2h | Non-modifying |
| Standby Immediate | E0h | Non-modifying |
| Write Buffer | E8h | Modifying |
| Write DMA | CAh | Modifying |
| Write DMA Queued | CCh | Modifying |
| Write Multiple | C5h | Modifying |
| Write Sector(s) | 30h | Modifying |

266

267

SCSI

| Length | Command | Hex Code | Category |
|--------|-------------------|----------|---------------|
| 6 | Change Definition | 40h | Configuration |
| 6 | Compare | 39h | Non-modifying |
| 6 | Copy | 18h | Modifying |
| 6 | Copy and Verify | 3Ah | Modifying |
| 6 | Erase | 19h | Modifying |
| 10 | Erase | 2Ch | Modifying |
| 6 | Format Unit | 04h | Modifying |
| 6 | Inquiry | 12h | Non-modifying |
| 6 | Lock/Unlock Cache | 36h | Non-modifying |
| 6 | Log Select | 4Ch | Modifying |
| 6 | Log Sense | 4Dh | Modifying |
| 6 | Mode Select | 15h | Configuration |
| 10 | Mode Select | 55h | Configuration |
| 6 | Mode Sense | 1Ah | Non-modifying |
| 10 | Mode Sense | 5Ah | Non-modifying |
| 6 | Pre-Fetch | 34H | Configuration |

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| Length | Command | Hex Code | Category |
|--------|------------------------------|----------|---------------|
| 6 | Prevent/Allow Medium Removal | 1Eh | Non-modifying |
| 6 | Read Block Limits | 05H | Non-modifying |
| 6 | Read Buffer | 3Ch | Non-modifying |
| 6 | Read Capacity | 25h | Non-modifying |
| 10 | Read Defect Data | 37h | Non-modifying |
| 6 | Read Generation | 29h | Non-modifying |
| 6 | Read Long | 3Eh | Non-modifying |
| 6 | Read Reverse | 0Fh | Non-modifying |
| 6 | Read Updated Block | 2Dh | Non-modifying |
| 6 | Read | 08h | Non-modifying |
| 10 | Read | 28h | Non-modifying |
| 6 | Reassign Blocks | 07h | Modifying |
| 6 | Receive Diagnostic Results | 1Ch | Non-modifying |
| 6 | Recover Buffered Data | 14h | Non-modifying |
| 6 | Release | 17h | Non-modifying |
| 6 | Request Sense | 03h | Non-modifying |
| 6 | Reserve | 16h | Non-modifying |
| 6 | Rezero Unit | 01h | Configuration |
| 10 | Search Data Equal | 31h | Non-modifying |
| 10 | Search Data High | 30h | Non-modifying |
| 10 | Search Data Low | 32h | Non-modifying |
| 6 | Seek | 0Bh | Non-modifying |
| 10 | Seek | 2Bh | Non-modifying |
| 6 | Send Diagnostic | 1Dh | Non-modifying |
| 10 | Set Limits | 33h | Configuration |
| 6 | Set Window | 24h | Configuration |
| 6 | Space | 11h | Configuration |
| 6 | Start/Stop Unit | 1Bh | Non-modifying |
| 6 | Synchronize Cache | 35h | Configuration |
| 6 | Test Unit Ready | 00h | Non-modifying |
| 6 | Verify | 13h | Non-modifying |
| 10 | Verify | 2Fh | Non-modifying |
| 10 | Write and Verify | 2Eh | Modifying |
| 6 | Write Buffer | 3Bh | Modifying |
| 6 | Write Filemarks | 10h | Modifying |
| 6 | Write Long | 3Fh | Modifying |
| 6 | Write Same | 41h | Modifying |
| 6 | Write | 0Ah | Modifying |
| 10 | Write | 2Ah | Modifying |

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