Carbon Nanotube Digital Electronics Workshop

September 6, 2012

AGENDA

7:45am	Transportation from Hilton to NIST
8:00am	Registration + Coffee
	Opening Remarks
8:30am	*Welcome* – John Suehle, NIST
8:40am	*What is Needed from a CNT Technology* – Wilfried Haensch, IBM
9:10am	*Challenges to Realizing a CNT Technology* – Jeff Bokor, UC-Berkeley
9:40am	BREAK – 30 min
	Circuits
10:10am	CNT Circuit Design—Dealing with Variations – Subhasish Mitra, Stanford
10:40am	CNT Circuits at VDD < 0.4 V – Lian-Mao Peng, Peking Univ.
	Device
11:10am	*Contacts and NFETs* – Chongwu Zhou, USC
11:40pm	*Scaling and Variability* – Aaron Franklin, IBM
12:10pm	LUNCH – 60 min
1:10pm	*Performance Projections from CNTFET Modeling* – Francois Leonard, Sandia
	Separation/Isolation of Semiconducting CNTs
1:40pm	*DNA-driven Separation* – Xiaomin (Helen) Tu, NIST
2:10pm	*Density Gradient Ultracentrifugation Separation* – Mark Hersam, Northwestern
2:40pm	BREAK – 30 min
3:10pm	*Size-Exclusion Chromatography Separation* – George Tulevski, IBM
	Controlled Placement of CNTs
3:40pm	*Aligned CNT Synthesis* – Jie Liu, Duke
4:10pm	*Placement Using Ion-Exchange Chemistry* – Hongsik Park, IBM
	Closing Remarks
4:40pm	*Discussion and Summary* – Philip Wong, Stanford
5:00pm	Transportation from NIST to Hilton for Happy Hour