



P. Risterucci¹, E. Martinez¹, R. Boujamaa², J. Zegenhagen³, B. Detlefs^{3,1}, M. Gros-Jean², O. Renault¹ and S. Tougaard⁴

¹ CEA, LETI, MINATEC Campus, 17 rue des Martyrs, 38054 Grenoble Cedex 9, France

² STMicroelectronics, 850 rue Jean Monnet, Crolles 38926, France

³ European Synchrotron Radiation Facility, 6 rue Jules Horowitz, Grenoble F-38000, France

⁴ Department of Physics and Chemistry, University of Southern Denmark, DK-5230 Odense M, Denmark

Objectives

HAXPES offers new possibilities regarding non-destructive characterization of gate stacks in technologically relevant conditions. Current developments aim at further increasing the depth sensitivity by analyzing the inelastic background (Tougaard's method).

Experimental details

HAXPES at ID32 – ESRF [1]

- High photon energy (2.1-25 keV)
- High energy Resolution (< 0.4 eV)
- High sampling depth (> 50 nm)
- High in-depth sensitivity (< 1 nm)
- Optimized geometry & instrumentation

HAXPES chamber at ID32

Non-destructive analysis of chemical & electronic properties of buried layers and interfaces

High-k/metal gate stack for the 32 nm node

Poly-Si 30 & 50 nm
TiN 6.5 nm
LaO (0, 0.4 & 1 nm)
HfSiON 1.7 nm
SiON 1.5 nm
Si

40 < t < 60 nm

Impact of spike anneal (1065°C)

- Diffusion of La ?
- Chemistry of high-k/Si interface ?

High-k/metal gate stack with TiN

After anneal: La silicate at high-k/Si interface [2]

Shift of Hf 4f after anneal : interfacial dipole [3]

Before anneal After anneal

La diffusion towards the high-k/Si interface

La00-R: no La
La04-R: LaO (0.4 nm)
La10-R: LaO (10 nm)

- La diffusion through the high-k
- La silicate (La-O-Si) at the high-k/Si interface
- Interfacial dipole, strength increasing with LaO thickness

High-k/metal gate stack with TiN and poly-Si

$h\nu = 15/18$ keV - high in-depth sensitivity (~60 nm)

Inelastic background modelling using Tougaard's method [4] + error calculation (weighed χ^2)

as-dep: La in-depth location

36 ± 6.5 nm

36 ± 3 nm

annealed: La in-depth location

71 ± 4.5 nm

27.5 ± 2 nm

Extraction of in-depth elemental profiles

Conclusions

- The capabilities of HAXPES for analysis of advanced high-k/metal gate stacks are demonstrated
 - Non-destructive analysis
 - High sampling depth (> 50 nm)
 - High in-depth sensitivity (1 nm)
 - Extraction of elemental profiles
- This paves the way to increased analysis reliability thanks to measurements without sample preparation (sputtering, chemical etching).

References

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