



Scanning tunneling microscopy of gate tunable topological insulator Bi_2Se_3 thin films

Tong Zhang,^{1,2} Niv Levy,¹ Jeonghoon Ha,^{1,2,3} Young Kuk,³ and Joseph A. Stroscio^{1,*}

¹Center for Nanoscale Science and Technology, NIST, Gaithersburg, Maryland 20899, USA

²Maryland NanoCenter, University of Maryland, College Park, Maryland 20742, USA

³Department of Physics and Astronomy, Seoul National University, Seoul 151-747, Korea

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Electrical-field control of the carrier density of topological insulators (TIs) has greatly expanded the possible practical use of these materials. However, the combination of low-temperature local probe studies and a gate tunable TI device remains challenging. We have overcome this limitation by scanning tunneling microscopy and spectroscopy measurements on *in situ* molecular-beam epitaxy grown Bi_2Se_3 films on SrTiO_3 substrates with prepatterned electrodes. Using this gating method, we are able to tune the Fermi level of the top surface states within a range of ≈ 250 meV on a 3-nm-thick Bi_2Se_3 device. We report field effect studies of the surface-state dispersion, band gap, and electronic structure at the Fermi level.

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Three-dimensional (3D) topological insulators (TIs) are a novel state of matter which has a bulk band gap but topologically protected metallic surface states.^{1,2} Angle-resolved photoemission (ARPES)²⁻⁶ and scanning tunneling microscopy (STM)⁷⁻¹⁰ have confirmed the existence of these surface states in various compounds. These protected surface states are helical Dirac fermions which are predicted to host many striking quantum phenomena.^{1,2} However, in order to fully utilize the unique properties of these surface states, the Fermi level (E_F) needs to be close to the Dirac point (E_D) and tunable across it. Unfortunately, three-dimensional TIs, like Bi_2Se_3 and related materials, are usually heavily doped narrow gap semiconductors, with the Fermi level away from the Dirac point. While bulk and surface chemical doping have been used to tune E_F , it is preferable to tune the carrier density using a gate induced electric field, as recently demonstrated in transport experiments.¹¹⁻¹⁶ In addition to simply shifting E_F , the electric field is capable of altering the surface band structure of ultrathin TI films, where the top and bottom surface states are coupled, and a hybridization gap opens at the Dirac point.¹⁷⁻²⁰ For example, it might be possible to observe a topological phase transition in these systems by applying an electric field perpendicular to the plane of the film, as suggested by recent calculations.²⁰

The combination of local probe studies of the density of states using STM and a gate tunable TI device is difficult, mostly due to the environmental sensitivity of TI materials.²¹ Unlike graphene, *ex situ* fabrication and processing will significantly degrade the surfaces of TI materials, making them inaccessible to STM. In this work, we demonstrate the fabrication of gate tunable 3D TI devices that are suitable for STM studies. Thin Bi_2Se_3 films are epitaxially grown on SrTiO_3 (STO) (111) substrates prepatterned with Pt electrodes, which are mounted on specially designed sample holders (Fig. 1). The preparation of the STO substrates is crucial for good quality film growth and a gate insulator that maintains high resistance. The STO (111) substrates were cleaned by the method described in Ref. 22. STO pieces, $(3 \times 4 \times 0.1)$ mm³, were immersed in hot deionized water (70 °C) with ultrasonic agitation for 30 min. The pieces were then annealed in a tube furnace at 1000 °C for 1 h under a pure O_2 atmosphere.

After this two-step treatment, the STO surface is completely cleaned and ambient atomic force microscopy (AFM) images show flat and regular terraces [Fig. 2(a)]. Additionally this treatment results in highly insulating STO, which is crucial for using it as a gate dielectric. After cleaning, two rectangular Pt electrodes (50 nm thick) were deposited on the top surface of the STO substrate to ensure good electrical contacts to the TI film. Another Pt electrode, serving as a back gate, was deposited on the bottom surface of STO. The prepatterned STO piece was mounted in an Al_2O_3 based sample holder which has multiple tungsten clips for sample wiring and a tungsten spring clip to hold the sample, which also doubles as a gate contact (Fig. 1). The whole device is fully compatible with ultrahigh vacuum and can be degassed to 600 °C. Prior to film growth, the STO and sample holder were degassed for 30 min at 500 °C to remove adsorbed gases. The reflection high-energy electron diffraction (RHEED) pattern gave clean 3×3 surface reconstruction patterns of STO (111) [Fig. 2(b)]. Growth of Bi_2Se_3 films was carried out by co-evaporating pure elemental (99.9999%) Bi and Se from Knudsen cells, using a 1:10 flux ratio (Bi:Se) to reduce Se vacancies. The STO substrates were kept at 250 °C during growth. RHEED patterns [Fig. 2(c)] measured during growth show 1×1 streaks, an indication of good crystal quality. The samples were then transferred *in situ* into a STM directly after growth, which avoids any *ex situ* post processing. The experiments were performed in a custom designed STM operating at 5 K, which is connected to the MBE systems.²³ Due to the large dielectric constant of STO at low temperatures ($\approx 10^4$), we were able to change the carrier density on the order of 10^{13} cm⁻² at a gate voltage of 100 V. Note, that this corresponds to a similar density change in graphene obtainable with a gate insulator of 300-nm-thick SiO_2 at 100 V, but we are able to reach similar densities with 100- μm -thick STO crystal due to the large dielectric constant.

Bi_2Se_3 has a layered structure consisting of Se-Bi-Se-Bi-Se quintuple layers (QLs), where the bonding between adjacent quintuple layers is weak.²⁴ In this paper we focus on three QL thick Bi_2Se_3 samples, since gating is more effective on thinner films. Figure 2(d) shows the STM topography of a nominal three-QL-thick film with flat terraces and 1-nm step heights, corresponding to one QL. Atomic resolution measurements are

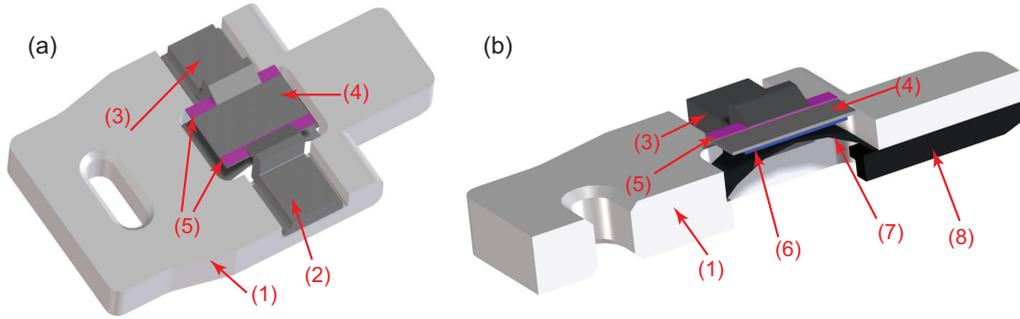


FIG. 1. (Color online) 3D computer-automated drawings of the high-temperature sample holder with *in situ* back gating capability. (a) Top view. (b) Cross-sectional view. Part list: (1) alumina sample holder, (2) W source/drain electrode (tunnel bias), (3) W source/drain electrode, (4) SrTiO₃ substrate, (5) Pt electrodes on SrTiO₃, (6) Pt back gate electrode on SrTiO₃, (7) W spring clip to hold SrTiO₃, (8) W back gate electrode.

shown in the Fig. 2(d) inset. Figure 3(a) shows the two-terminal resistance measured during sample cooldown, which displays an insulating behavior with resistance increasing about two orders of magnitude between room temperature and 5 K. Similar insulating behavior has always been seen in ultrathin Bi₂Se₃ films, which could be due to strong interactions,²⁵ or Anderson localization.^{26,27} To test the gating ability of

the TI/STO device, we measured the two-terminal resistance through the three-QL film vs gate voltage [Fig. 3(b)]. The resistance increases at negative gate voltage as expected for

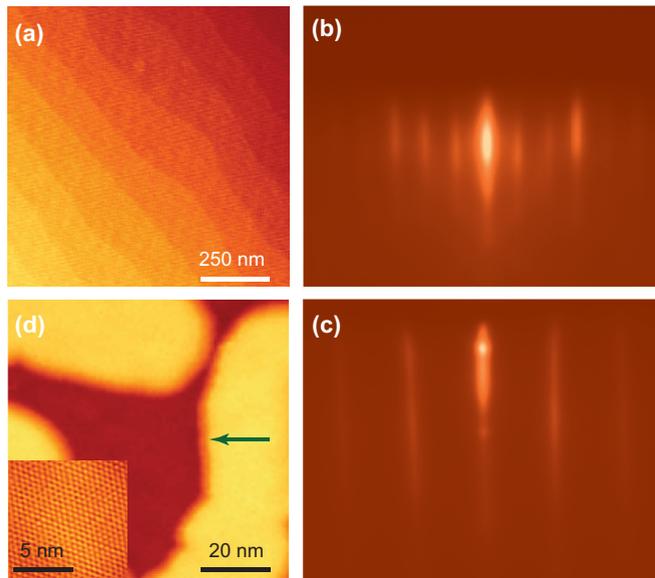


FIG. 2. (Color online) Characterization of the SrTiO₃ substrate and TI growth. (a) AFM image, 1 μm × 1 μm, of SrTiO₃ after thermal processing as detailed in the text showing large atomically flat terraces separated by single atomic-height steps. The AFM topographic height is shown in a color scale from dark to bright covering a range of 5.9 nm. (b) RHEED pattern of SrTiO₃ prior to TI growth. (c) RHEED pattern of three-QL Bi₂Se₃ film grown on SrTiO₃. (d) STM topographic image, 81 nm × 81 nm, of three-QL Bi₂Se₃ film grown by MBE on the gated sample holder in Fig. 1. The arrow indicates the position of *dI/dV* mapping of quasiparticle scattering in Fig. 5. Tunneling parameters: $V_B = 1.5$ V, $I = 30$ pA. Inset: 10 nm × 10 nm atomic resolution image of three-QL Bi₂Se₃. Tunneling parameters: $V_B = 0.5$ V, $I = 30$ pA. The STM topographic height is shown in a color scale from dark to bright covering a range of 2 nm for (d) and 0.03 nm for the inset.

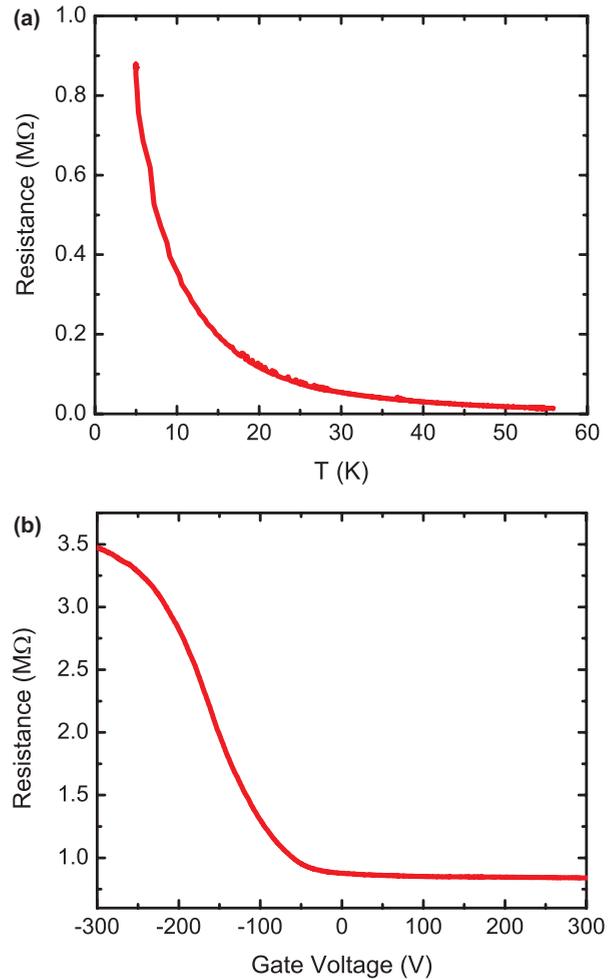


FIG. 3. (Color online) Transport properties of three-QL Bi₂Se₃ film. (a) Bi₂Se₃ film (three QL) two-terminal film resistance versus temperature. (b) Bi₂Se₃ film (three QL) two-terminal film resistance versus gate voltage. The resistance in (a) and (b) is the total film resistance using contacts 2 and 3 in Fig. 1 and therefore includes contact resistance contributions.

an n -type doped grown film. However, a maximum is not observed which may indicate that we were unable to place the Dirac point at the Fermi level even with -300 V applied to the gate electrode [Fig. 3(b)], but this may also be a signature of more complex interplay between the top and bottom surface transport and bulk transport.^{13,16}

The gating effect was locally characterized by scanning tunneling spectroscopy. The tunneling conductance dI/dV is measured by standard lock-in techniques and is shown in Fig 4(a) for different gate voltages V_G . At $V_G = 0$ V, there is a V-shaped structure in the dI/dV spectra with a minimum at a sample bias $V_B \approx -0.4$ V, which is typical for Bi_2Se_3 and was presumably attributed to the surface Dirac cone.^{9,10} This midgap minimum conductivity point serves as a convenient reference energy and will be referred to as the Dirac point, even though a Dirac cone does not strictly exist when the top and bottom surface states are hybridized, as is the case for these thin films. This point evolves into a Dirac point with thicker films. An additional feature seen in the spectra is a peak at $V_B \approx -0.5$ V at $V_G = 0$ V, which is likely caused by a quantum well (QW) state of the thin film, as seen in similar 3D TIs.²⁸ This peak is followed by a strong upturn due to the

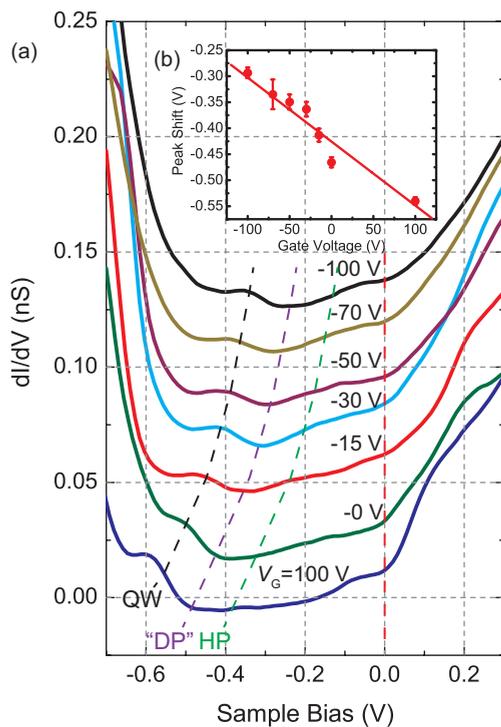


FIG. 4. (Color online) Electric-field effect of Bi_2Se_3 . (a) dI/dV spectra of three-QL Bi_2Se_3 film versus gate voltage. The spectra are shifted vertically for clarity. The dashed lines are guides to the eye to indicate the approximate positions of the quantum well state peak (QW, black), the Dirac point, or more strictly, the middle of the hybridized gap (DP, purple), and the hump peak that defines the surface band gap (HP, green). Tunnel parameters: $I = 100$ pA, $V_B = 0.3$ V, $V_{\text{mod}} = 10$ mV. (b) The shift of the leftmost QW peak versus gate voltage. The error bars are one standard deviation uncertainty in the peak position obtained by fitting the QW peak to a Lorentzian function. The solid line is a linear fit yielding the gating efficiency of (1.2 ± 0.1) mV/V. The uncertainty in slope is one standard deviation uncertainty determined from the linear fit.

onset of the valence-band top. At positive bias we also observe a kink in the spectrum at E_F ($V_B = 0$ V) followed by an upturn.

Application of a gate electric field causes a shift of the spectra as observed in Fig. 4(a). A downward sweep of gate voltage is accompanied by the Dirac point shifting to higher energies relative to the Fermi level at 0 V, which is a direct signature of tuning of the carrier density by the applied electric field. To quantify the gating tunability we plot the peak position of the QW peak vs gate voltage in Fig. 4(b). A linear fit to the data in Fig. 4(b) gives a gating efficiency of (1.2 ± 0.1) mV/V.²⁹ At $V_G = 100$ V, the Dirac point shift is 120 meV [according to the linear fitting in Fig. 4(b)], which gives a density change of surface carriers of $\approx 0.4 \times 10^{13}$ cm⁻² (estimated by assuming a linear Dirac spectrum with velocity 5×10^5 m/s). To properly model the carrier density tuning and shift of the surface-state spectrum requires accounting for charges on both top and bottom surfaces, in the bulk, as well as any band bending and screening, which is beyond the scope of this paper. An order of magnitude estimate can be made assuming a simple capacitor model where we expect a gate capacitance of $C_G = \epsilon_0 \epsilon / t = 620 \mu\text{F}/\text{m}^2$, where ϵ_0 is the permittivity of free space, t is the STO thickness (0.1 mm), and $\epsilon \approx 7000$ is the dielectric constant of STO.³⁰ This gate capacitance should induce a total charge density at the surface of the dielectric of $\approx 4 \times 10^{13}$ cm⁻² at $V_G = 100$ V. However, we observe an induced surface carrier density that is ten times less, which is not sufficient to overcome the initial film doping and place the Dirac point at E_F at $V_G = -100$ V. We attribute the low gating efficiency of the top surface states to the degenerately doped bulk of the Bi_2Se_3 film in combination with the bottom surface state, which screens the electric field reaching the top surface. Thus the shift of the bottom surface's Dirac point may be larger than that of the top surface.¹³ The high initial doping of the film is probably due to defects produced in growth, which is common in MBE growth of Bi_2Se_3 films as well as bulk synthesized crystals. The mixed compound $\text{Bi}_2\text{Te}_2\text{Se}$ has been found to be a more insulating material,³¹ and hence may respond better to our gating scheme.

In a three-QL Bi_2Se_3 film, a hybridized gap is expected to open at the Dirac point due to the coupling of top and bottom surface states,¹⁷ which has been observed in ARPES measurements.⁶ Evidence of a gap opening can be seen in the spectra in Fig. 4(a), where a depression is seen between the QW state peak and the broad hump to the right of the Dirac minimum, as outlined by the dashed lines. If we associate this feature with the expected gap, its width of approximately 160 meV roughly matches the ARPES data.⁶ However, the feature is less defined when the spectra shifts to larger negative energies and higher doping, hence a strict association with the hybridized gap may be tenuous.

In addition to gate dependent features in the spectra, we observe a gate independent kink in the tunneling spectra at E_F [Fig. 4(a)] at positive bias. Similar DOS kinks around E_F have been widely observed in Bi_2Se_3 , for both thin-film and bulk samples,^{9,10} and were typically considered to be the onset of the conduction band. However, the gate independence of the feature shown here contradicts that explanation and possibly suggests that it stems from a different mechanism, such as the many-body effects in 2D Dirac systems.^{32,33}

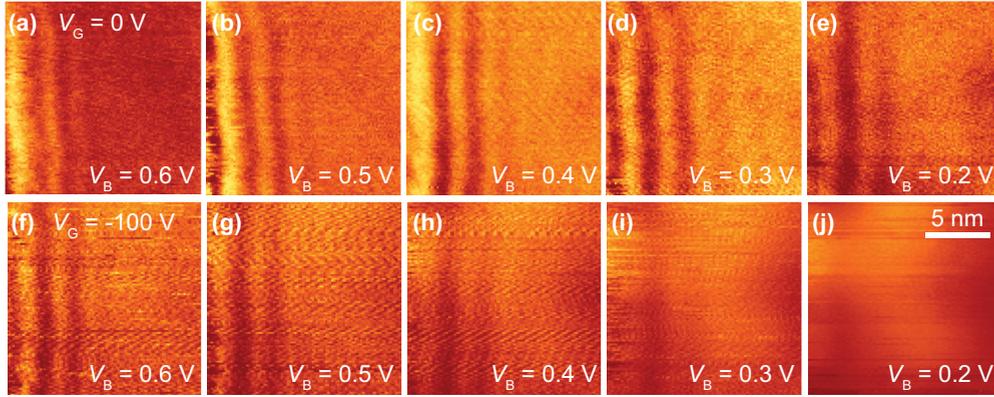


FIG. 5. (Color online) Bi_2Se_3 quasiparticle interference (QPI) patterns from the step edge indicated in Fig. 2(d). dI/dV maps of QPI patterns at $V_G = 0$ V (a)–(e) and $V_G = -100$ V (f)–(j) at the indicated sample biases. dI/dV intensity is given by the color scale from low (dark) to high (bright) in arbitrary units. The oscillations in the dI/dV maps are used to determine the energy momentum dispersion in Figs. 6 and 7. Tunneling parameters: $I = 50$ pA, V_B indicated in each panel, $V_{\text{mod}} = 15$ mV.

One possible mechanism would involve the surface plasmon mode which has been predicted to exist in graphene and TI surfaces.³² Below E_F , the electron-plasmon interaction can lead to reconstruction of the Dirac cone by forming a plasmaron band, which gives a sudden DOS change at Fermi level.³² This mechanism, which depends on interactions far

from the Dirac point, may remain intact despite the opening of a gap around it,¹⁸ and may even be enhanced in ultrathin film geometries.²⁵

To further explore the gating effects on the surface-state band structure, we studied the quasiparticle interference (QPI) patterns in spectroscopic mapping at different gate voltages.

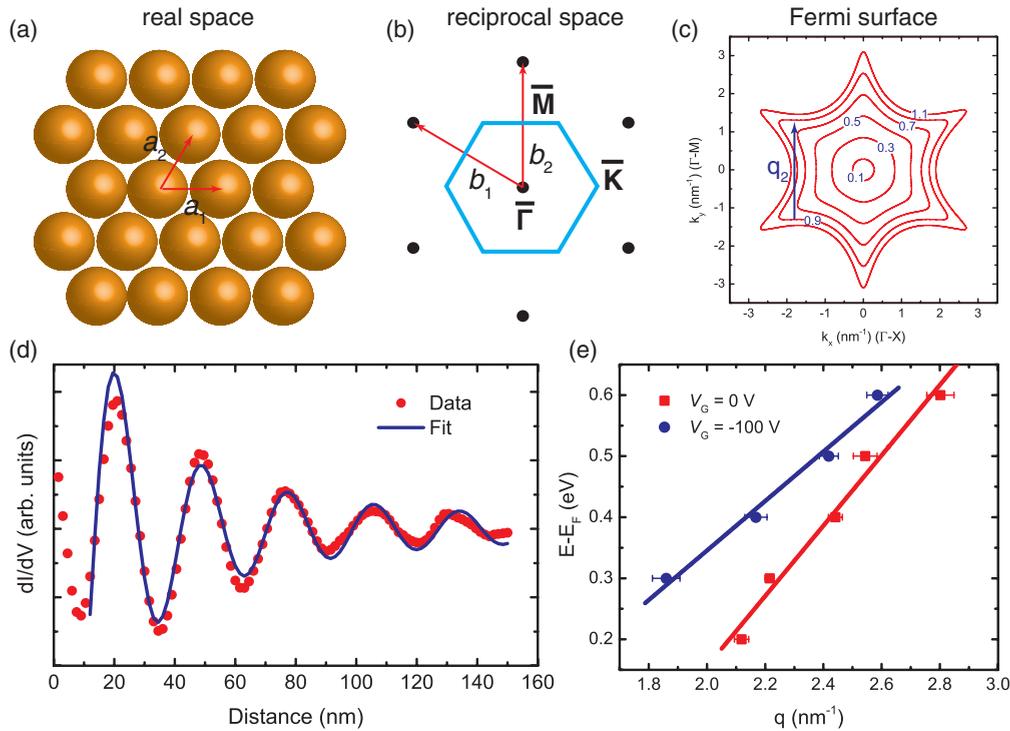


FIG. 6. (Color online) Quasiparticle scattering and LDOS oscillations from Bi_2Se_3 as a function of back gating. (a) Real-space Bi_2Se_3 (111) surface lattice. Step edges occur along closed-packed directions indicated along the a_1 unit-cell vector. (b) Corresponding reciprocal-space lattice and surface Brillouin zone (blue hexagon). Scattering at the step edges correspond to scattering in the $\bar{\Gamma}$ - \bar{M} direction. (c) Fermi-surface contours given by Eq. (1) for energies relative to the Dirac point of 0.1–1.1 eV. Scattering oscillations are dominated by the vector q_2 connecting extremal parts of the Fermi contours along the $\bar{\Gamma}$ - \bar{M} direction. (d) dI/dV intensity oscillations from scattering at the step edge in Fig. 2(d) obtained from the map in Fig. 5(d) at $V_B = 0.3$ V and $V_G = 0$ V. A smooth background was subtracted from the raw intensity to determine the oscillatory part (red data points), which was fit to a function $A \cos(qx)/x^{3/2}$ (blue line), where amplitude A and scattering vector q are fitting parameters. (e) Energy versus scattering vector obtained from dI/dV intensity oscillations as in (d) for gate voltages of 0 V and -100 V. The solid lines are linear fits to the data. The error bars are one standard deviation uncertainty in the scattering vector determined from fitting the intensity oscillations in (d).

QPI patterns develop from scattering off constant energy contours with intensity concentrated for scattering vectors that connect parallel or extremal sections of the contours. Figures 5(a)–5(e) and 5(f)–5(j) show the dI/dV mapping of the energy resolved local density of states (LDOS) at a step edge in Fig. 2(d) at $V_G = 0$ and $V_G = -100$ V, respectively. Within the bias range of $V_B = 0.2$ V to $V_B = 0.6$ V, standing-wave patterns at the step edges are clearly observed, while below 0.2 V we could not observe a clear interference pattern. Generally, at sample biases close to the Dirac point, scattering is weakened due to topological protection, despite the opening of a gap at the Dirac point.¹⁸ The data in Fig. 5 cover a bias range that is far from the Dirac point (0.5–1.0 eV above E_D), and the constant energy contours of the surface states are hexagonally warped at these energies,^{34,35} which enhances the scattering. Nevertheless, the QPI periods in Fig. 5 are clearly seen to depend on gate voltage.

We analyze these patterns by considering the scattering geometry and Fermi-surface contours in Fig. 6. Step edges occur along the closed packed directions in the lattice [Fig. 6(a)], and therefore the scattering in Fig. 5 across the steps in Fig. 2(d) represents scattering in the $\bar{\Gamma}-\bar{M}$ direction [Fig. 6(b)]. The energy contours for Bi_2Se_3 can be estimated from the energy dispersion which describes the warping given by³⁴

$$E(k)_\pm = \pm \sqrt{(vk)^2 + (\lambda k^3 \cos 3\theta_k)^2}, \quad (1)$$

where v is the Dirac velocity, λ is the warping parameter, and $\theta_k = \tan^{-1}(k_y/k_x)$ is the azimuthal angle of the momentum with respect to the x axis ($\bar{\Gamma}-\bar{K}$). The energy contours using Eq. (1) for Bi_2Se_3 with $\lambda = 128$ eV \AA^3 and $v = 3.55$ eV \AA (Ref. 35) are shown in Fig. 6(c). We note that even though the three-QL film opens a band gap at the Dirac point, the energy contours at 0.5 eV above the Dirac point are relatively independent of film thickness,³⁶ and therefore we expect the analysis using Eq. (1) and Fig. 6(c) will remain valid for the three-QL film. Above 0.3 eV relative to the Dirac point the contours deviate from circular symmetry. The data in Fig. 5 cover the energy range of approximately 0.6–1.0 eV where significant warping is observed in Fig. 6(c). In these warped contours the dominant scattering vector is q_2 connecting extremal parts of the Fermi surface [Fig. 6(c)].^{34,37,38} To determine the scattering vectors q_2 we fit the dI/dV intensity oscillations to a power-law decay, $A \cos(q_2 x)/x^{3/2}$,³⁸ as shown in Fig. 6(d). The resulting E vs q dispersion is plotted for two gate voltages in Fig. 6(e). The dispersions are offset from each other due to the shift of the Dirac point with gate potential. We recover the E vs k dispersion by connecting the scattering vectors to the momentum k . Recent calculations³⁸ have shown that $q_2 = 1.5 k$, where k is measured from $\bar{\Gamma}$. In addition, we convert the energy scale relative to the Dirac point instead of the Fermi level, using the measured Dirac point at $V_B = -0.4$ eV for $V_G = 0$ V and at $V_B = -0.27$ V for $V_G = -100$ V. The resulting dispersion is shown in Fig. 7, where

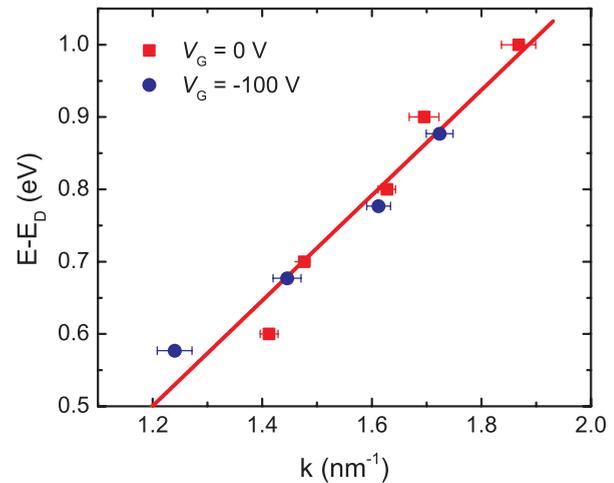


FIG. 7. (Color online) Bi_2Se_3 energy-momentum dispersion along the $\bar{\Gamma}-\bar{M}$ direction determined from quasiparticle scattering oscillations from step edges. The E vs q data in Fig. 6(e) at different gate voltages are reported with energies relative to the Dirac point at $V_B = -0.4$ V for $V_G = 0$ V and $V_B = -0.27$ V for $V_G = -100$ V. The momentum k is determined from the scattering vector as $q_2 = 1.5 k$ following Ref. 34. The data from the different gate voltages collapse onto the same dispersion. The solid line is a linear fit yielding a velocity $= (1.11 \pm 0.09) \times 10^6$ m/s. The error in the velocity is one standard deviation uncertainty from the linear fit.

both the $V_G = 0$ V and $V_G = -100$ V data follow a single curve when the energy is measured from the Dirac point. A linear fit to this data gives the local curvature of the E - k dispersion of the surface states with a velocity of $(1.1 \pm 0.1) \times 10^6$ m/s. This velocity is almost twice the velocity determined at lower energies on thick films with a linear dispersion.³⁸ At this high energy above the Dirac point the dispersion is expected to be no longer linear due to warping and also due to the finite band gap,⁶ which may account for the increase in the local velocity determined from these QPI measurements.

In summary, we have successfully fabricated *in situ* gate tunable epitaxial Bi_2Se_3 films. We have studied the effects of the gate's electric field as expressed in the shifts of spectral features in the tunneling spectra and in QPI scattering periods. While the current gating effectiveness is limited in Bi_2Se_3 due to bulk doping, the combination of *in situ* MBE, STM, and transport studies in TI devices opens new avenues for future work in more insulating samples.

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*Author to whom correspondence should be addressed: joseph.stroschio@nist.gov

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